

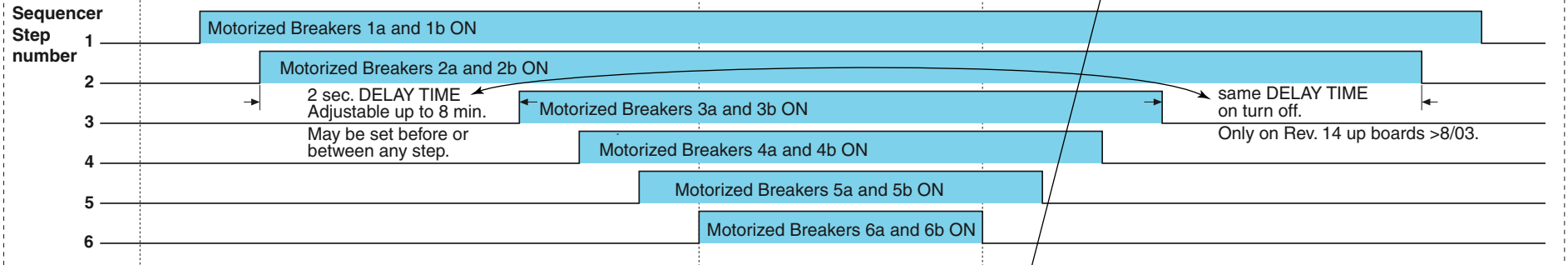
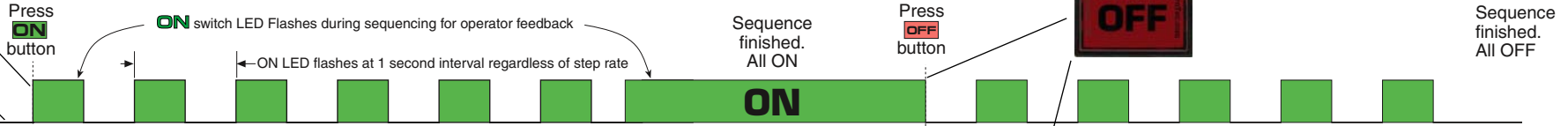
Overlay button images and alternate with +5v Logic

Download jpeg button images http://www.lyntec.com/LynTec_Buttons_jpg.zip

TIMING and LOGIC LEVEL DIAGRAM

LynTec MS-12 series Modular Sequencer

Timing Diagram is shown for a one board system with only one jumper in the **First 2 sec.** position. Step rates default at 1 sec.
 All boards within each panel are factory daisy-chain connected with 4 pin board-to-board cascade connectors.
 Remove cascade connectors to separate into multiple systems.



139-0266-09 MS-12 Timing Diagram 5/18/10

Control line TIMING, FUNCTIONS and LEVELS

